

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR**  
(AUTONOMOUS)

B.Tech. III Year II Semester Regular Examinations April-2026

**EMBEDDED SYSTEMS DESIGN**

(Common to CSE, CCC & CSIT)

**Time: 3 Hours**

**Max. Marks: 70**

**PART-A**

(Answer all the Questions 10 x 2 = 20 Marks)

- |   |   |  |     |    |    |
|---|---|--|-----|----|----|
| 1 | a | List any two purposes of embedded systems.                               | C01 | L1 | 2M |
|   | b | Define requirements analysis.  | C01 | L1 | 2M |
|   | c | What is a sensor? Give some example.                                     | C02 | L1 | 2M |
|   | d | Define a domain-specific processor.                                      | C02 | L1 | 2M |
|   | e | Why is ZigBee preferred in sensor networks?                              | C03 | L2 | 2M |
|   | f | Write the applications of I2C.   | C03 | L1 | 2M |
|   | g | What is embedded firmware?   | C04 | L1 | 2M |
|   | h | State advantages of high-level language-based firmware development.      | C04 | L1 | 2M |
|   | i | Name any two types of operating systems used in embedded systems.        | C05 | L1 | 2M |
|   | j | Mention the difference between preemptive and non-preemptive scheduling. | C05 | L2 | 2M |

**PART-B**

(Answer all Five Units 5 x 10 = 50 Marks)

**UNIT-I**

- |   |   |  |     |    |    |
|---|---|--|-----|----|----|
| 2 | a | Briefly explain about the History of Embedded Systems.               | C01 | L4 | 5M |
|   | b | Describe the classification of Embedded systems based on Complexity. | C01 | L2 | 5M |

OR

- |   |  |  |     |    |     |
|---|--|--|-----|----|-----|
| 3 |  | With a neat diagram, explain the design process of an embedded system. | C01 | L4 | 10M |
|---|--|--|-----|----|-----|

**UNIT-II**

- |   |   |   |     |    |    |
|---|---|---|-----|----|----|
| 4 | a | Describe the operation of Relay and Piezo buzzer. | C02 | L2 | 5M |
|   | b | Discuss in detail about 7-segment LED display.    | C02 | L2 | 5M |

OR

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|---|--|---|-----|----|-----|
| 5 |  | Explain general-purpose processors and domain-specific processors used in embedded systems. | C02 | L4 | 10M |
|---|--|---|-----|----|-----|

**UNIT-III**

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|---|---|---|-----|----|----|
| 6 | a | Differentiate between I2C and SPI communication protocols.      | C03 | L2 | 5M |
|   | b | Explain in detail about the USB and its types of data transfer. | C03 | L4 | 5M |

OR

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|---|--|--|-----|----|-----|
| 7 |  | Draw & explain the architecture of GPRS. | C03 | L4 | 10M |
|---|--|--|-----|----|-----|

**UNIT-IV**

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|---|---|--|-----|----|----|
| 8 | a | Write the advantages and limitations of the super loop approach. | C04 | L1 | 5M |
|   | b | Compare super loop-based approach and OS-based approach.         | C04 | L2 | 5M |

OR

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|---|--|---|-----|----|-----|
| 9 |  | Explain debugging and testing techniques used in embedded firmware development. | C04 | L4 | 10M |
|---|--|---|-----|----|-----|

**UNIT-V**

- |    |   |   |     |    |    |
|----|---|---|-----|----|----|
| 10 | a | Describe the concept of priority-based task scheduling. | C05 | L2 | 5M |
|    | b | Compare multiprocessing and multitasking.               | C05 | L2 | 5M |

OR

- |    |  |  |     |    |     |
|----|--|--|-----|----|-----|
| 11 |  | Explain task synchronization and the issues faced during task communication. | C05 | L4 | 10M |
|----|--|--|-----|----|-----|

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